FIG. 1

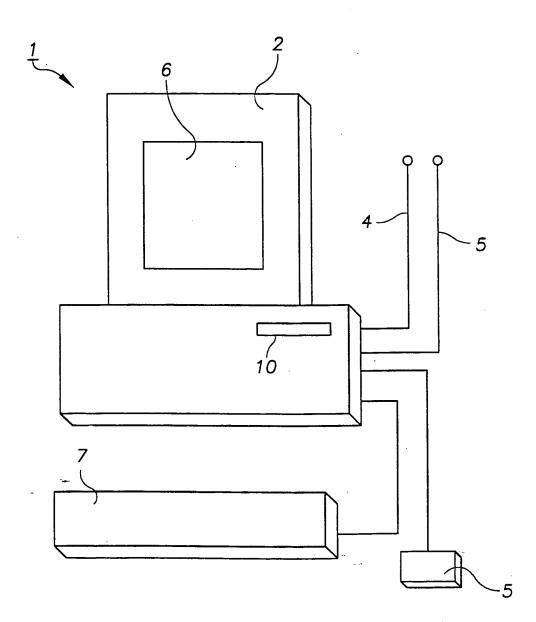
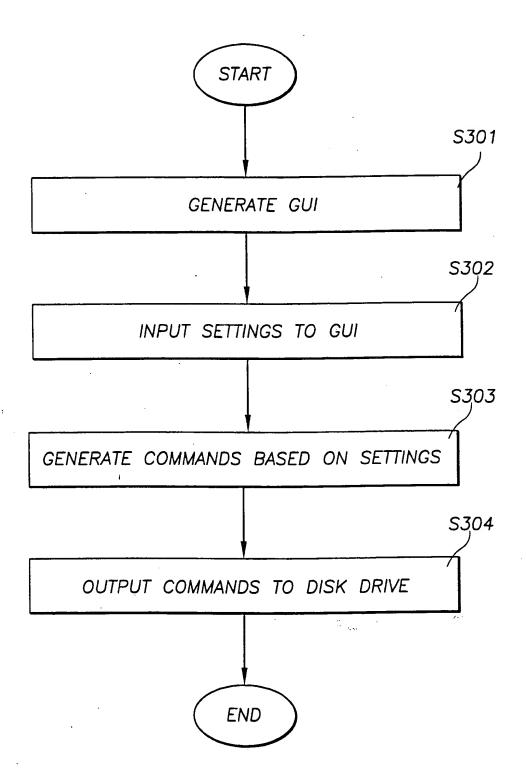
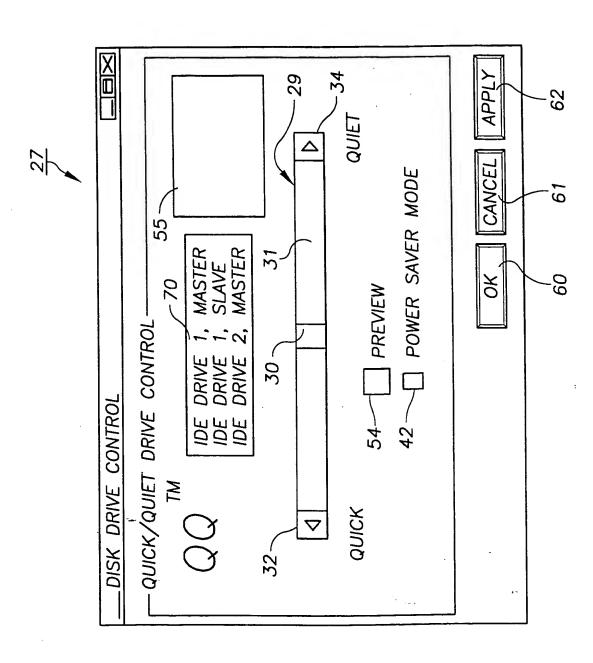
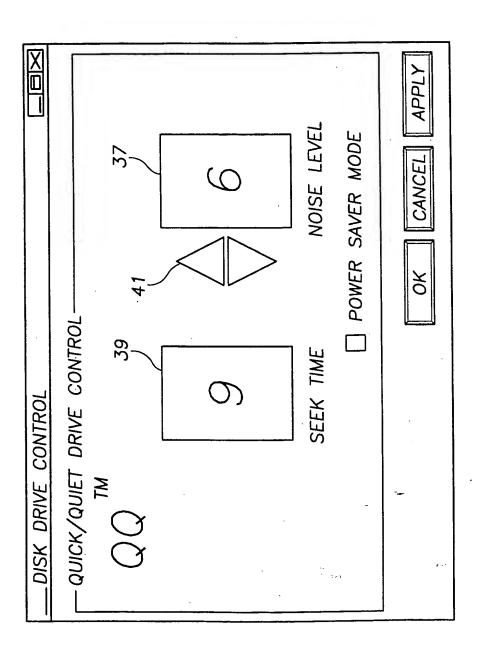


FIG.3

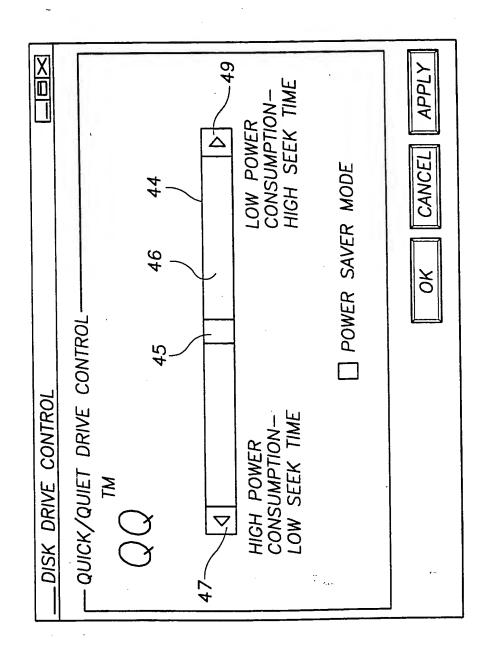




F1G.4



F1G.5



F1G.6

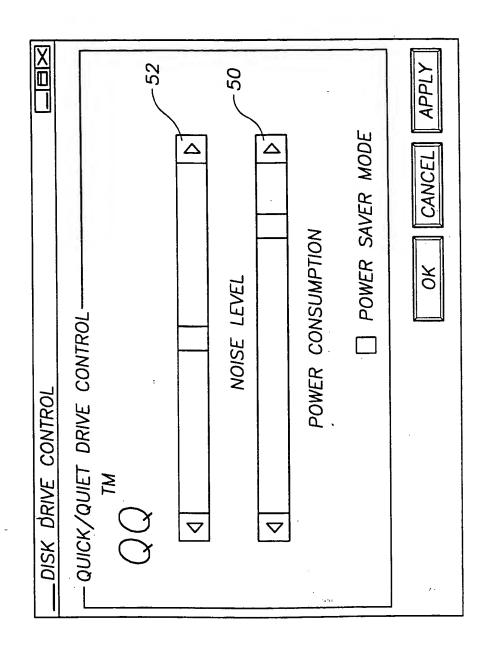
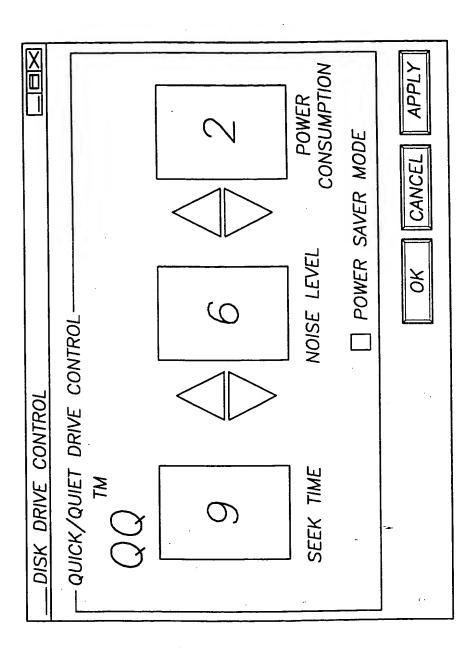


FIG.7



F1G.8

IDE DRIVE 1, MASTER

IDE DRIVE 1, SLAVE

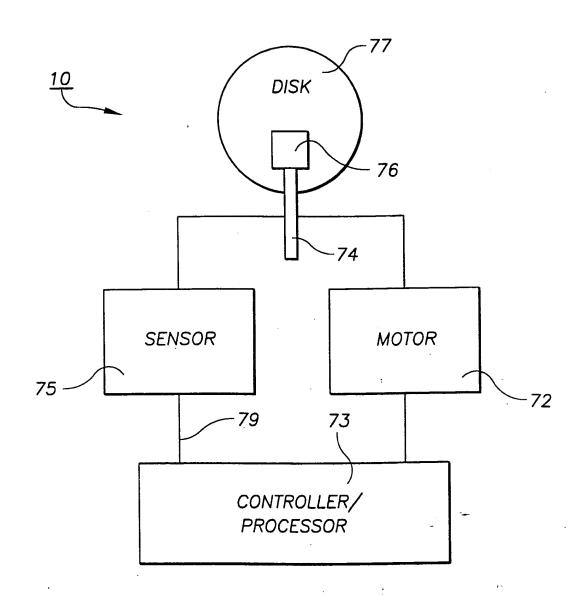
IDE DRIVE 2, MASTER

F1G.9

-

FIG. 10A

FIG. 10B



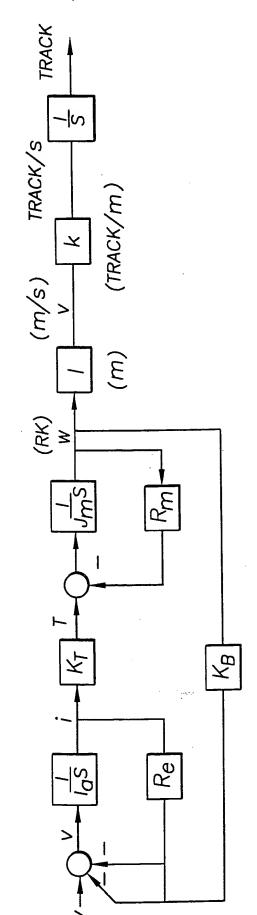


FIG. 11

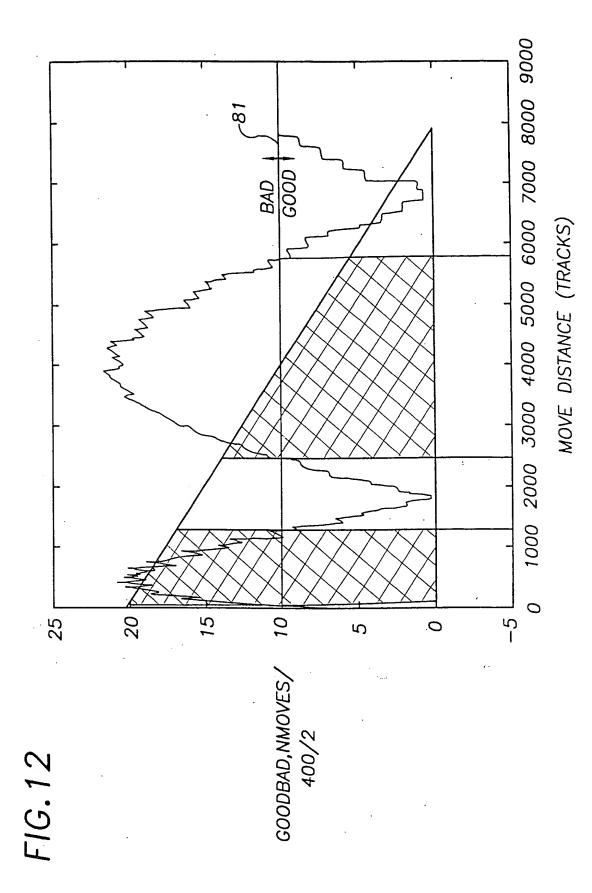


FIG. 13

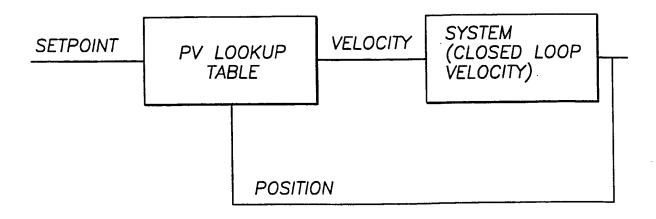


FIG. 14

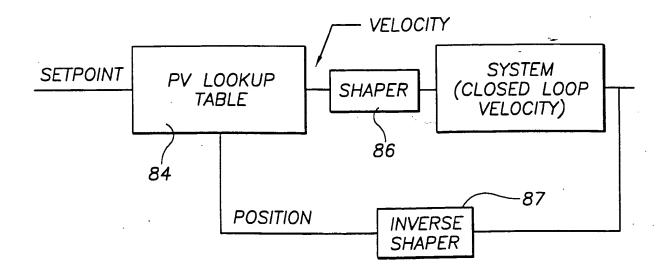


FIG. 15

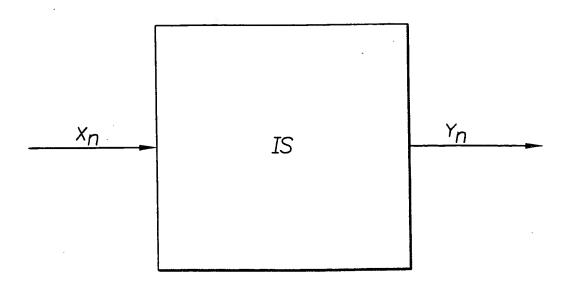


FIG. 16

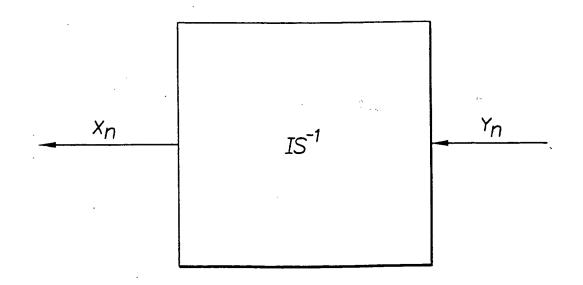


FIG. 17

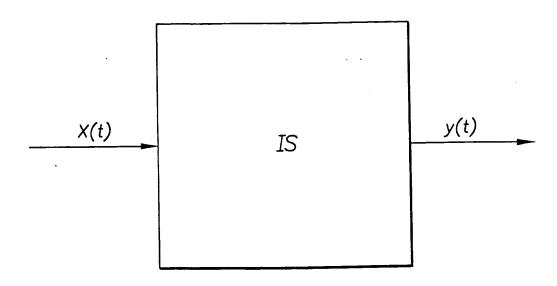


FIG18

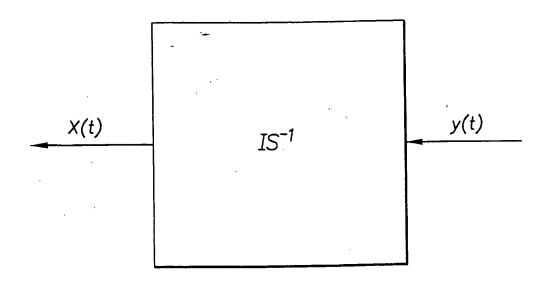


FIG.19

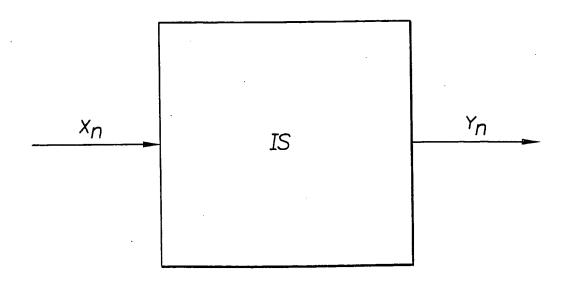
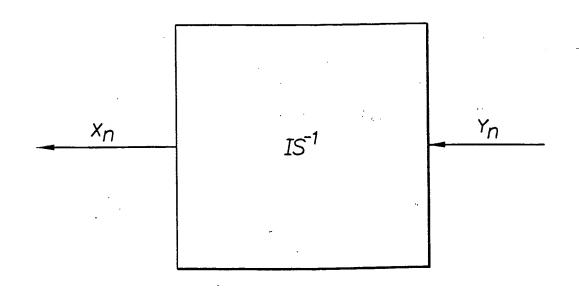
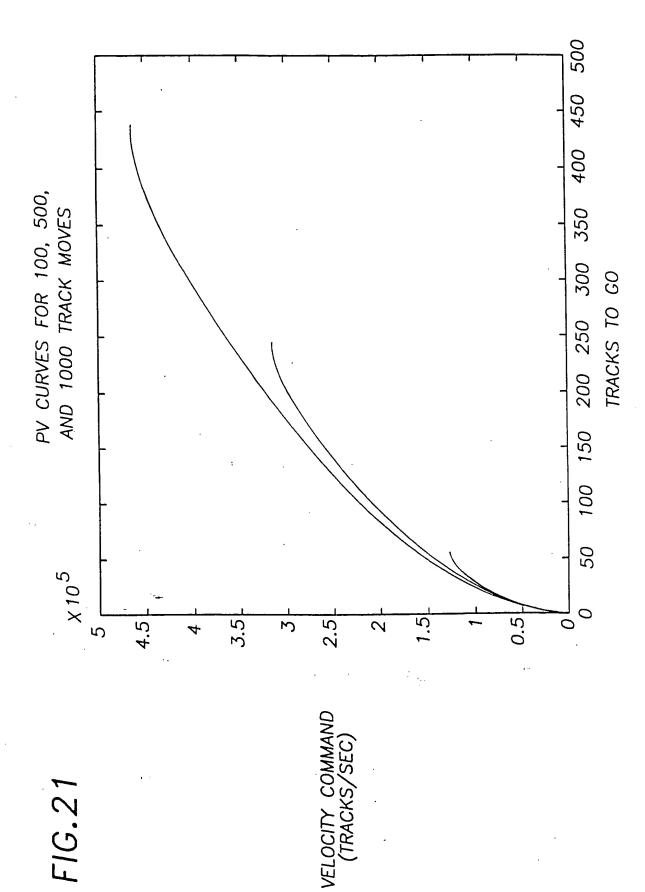
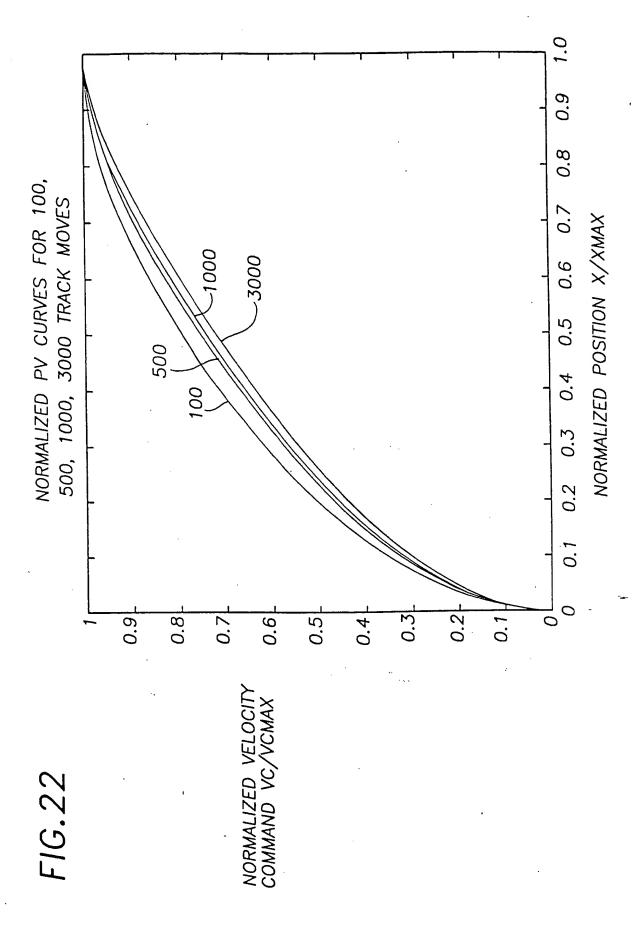
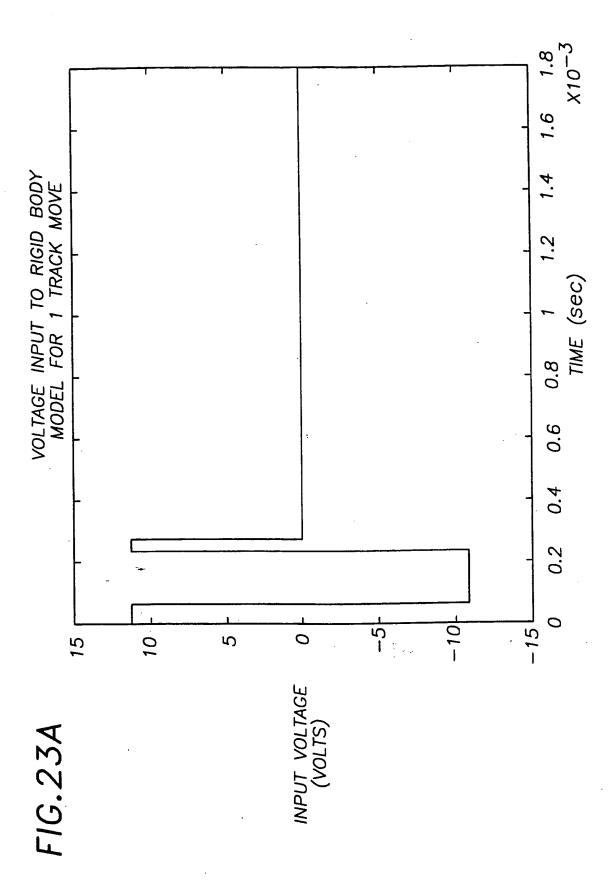


FIG20









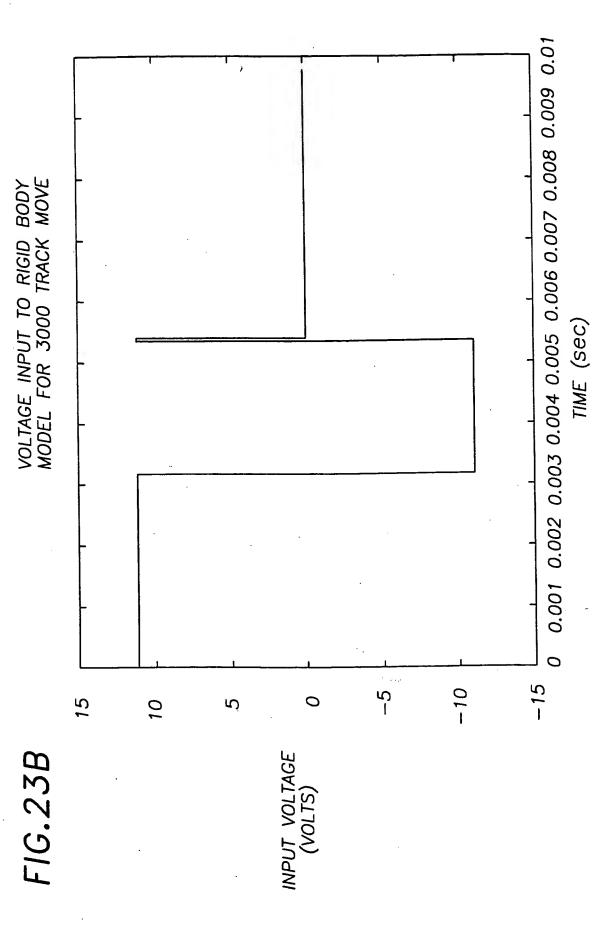


FIG.24

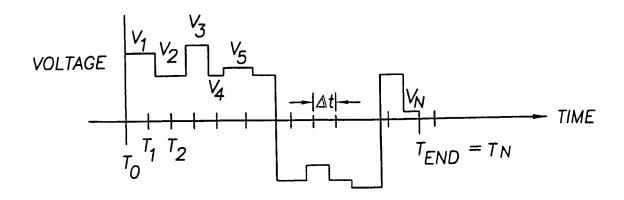


FIG.25

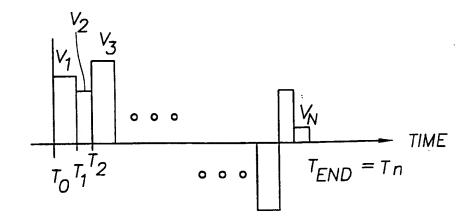


FIG.26

FIG.27

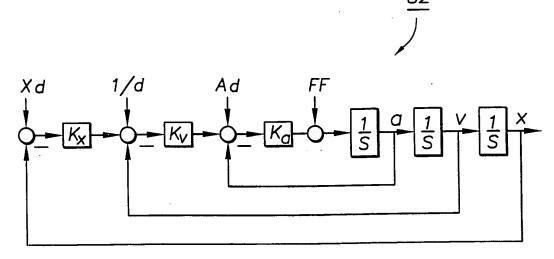
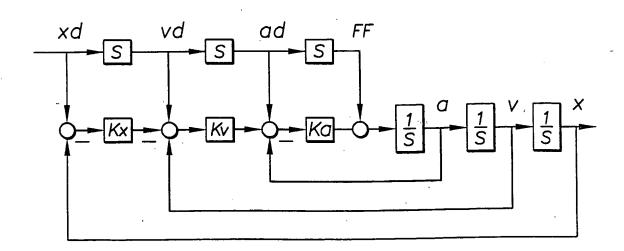


FIG.28



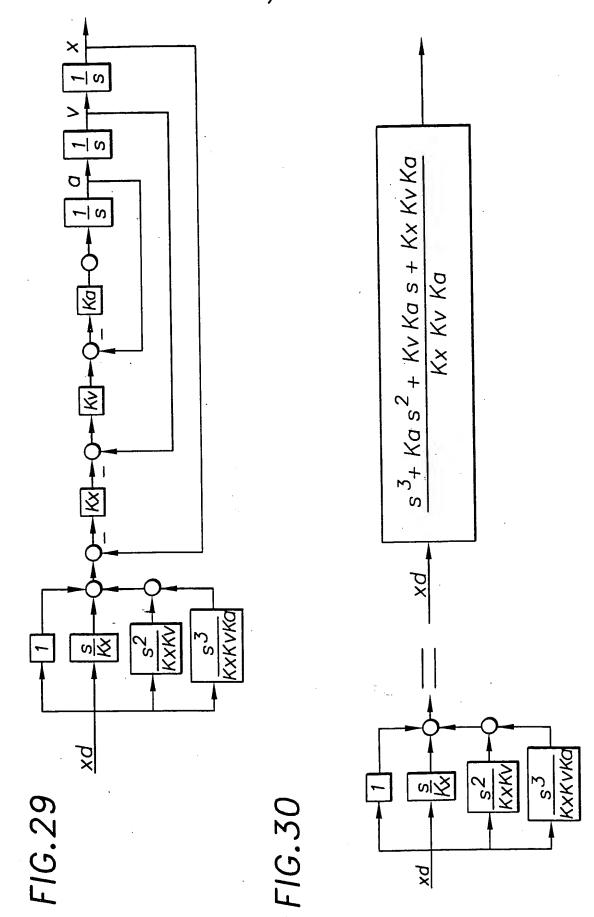


FIG. 31

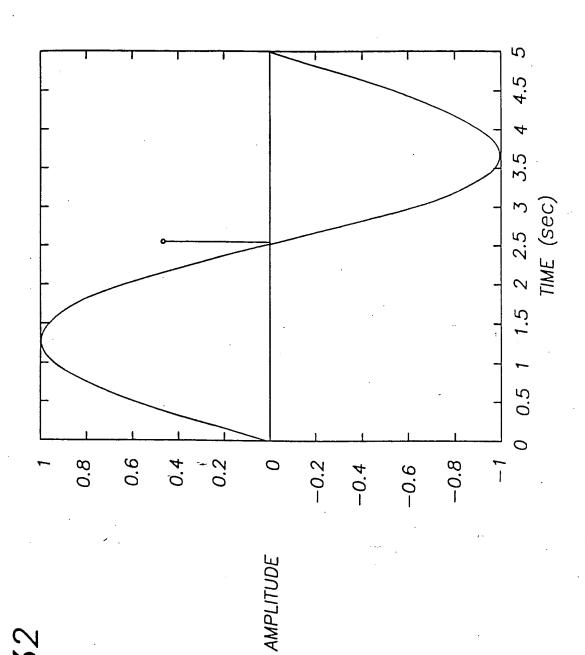


FIG.32

FIG.33

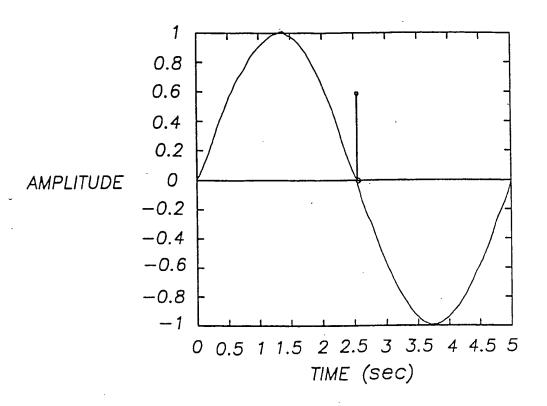


FIG.34

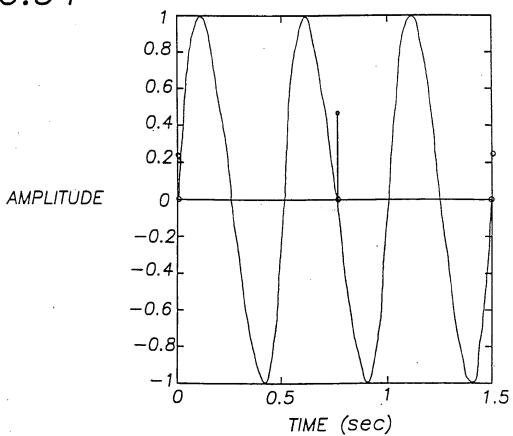


FIG.35

